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David Money Harris Associate Professor of Engineering at Harvey Mudd College in Claremont, CA, holds a Ph.D. from Stanford University and S.B. and M.Eng. degrees from MIT. His research interests include CMOS VLSI design, microprocessors, and computer arithmetic. He holds a dozen patents, is the author of three other books in the field of digital design and three hiking guidebooks, and has ...

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Design the architecture of a 4-bit capacitor DAC. 12.15 A bias generator is required to generate 16 steps from 0 to 100 μ A to bias an amplifier. Design a CMOS DAC to do this, assuming the presence of a 50 μ A reference current. 12.16 Differential circuits provide good noise immunity and have the advantage of dual rail inputs and outputs.

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David Money Harris Associate Professor of Engineering at Harvey Mudd College in Claremont, CA, holds a Ph.D. from Stanford University and S.B. and M.Eng. degrees from MIT. His research interests include CMOS VLSI design, microprocessors, and computer arithmetic. He holds a dozen patents, is the author of three other books in the field of digital design and three hiking guidebooks, and has ...

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