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Solution Manual Digital Signal Processing 1.1 Suppose the input to an amplifier is  $x_a(t) = \sin(2\pi F_0 t)$  and the steady-state output is  $y_a(t) = 100 \sin(2\pi F_0 t + \phi_1) - 2 \sin(4\pi F_0 t + \phi_2) + \cos(6\pi F_0 t + \phi_3)$  (a) Is the amplifier a

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**Digital Signal Processing - INAOE - P**  
1.1 Suppose the input to an amplifier is  $x_a(t) = \sin(2\pi F_0 t)$  and the steady-state output is  $y_a(t) = 100 \sin(2\pi F_0 t + \phi_1) - 2 \sin(4\pi F_0 t + \phi_2) + \cos(6\pi F_0 t + \phi_3)$  (a) Is the amplifier a linear system or is it a nonlinear system? (b) What is

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1. First fold  $x(n)$  and then delay the resulting signal by four samples. 2. First delay  $x(n)$  by four samples and then fold the resulting signal. (c) Sketch the signal  $x(-n + 4)$ . (d) Compare the results in parts (b) and (c) and derive a rule for obtaining the signal  $x(-n + k)$  from  $x(n)$ .

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